



晶采光電科技股份有限公司  
**AMPIRE CO., LTD.**

## **SPECIFICATIONS FOR LCD MODULE**

<b>CUSTOMER</b>	
<b>CUSTOMER PART NO.</b>	
<b>AMPIRE PART NO.</b>	<b>AG-320240A4STQW-TK6(N)(R)</b>
<b>APPROVED BY</b>	
<b>DATE</b>	

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## RECORD OF REVISION

Revision Date	Page	Contents	Editor
2007/1/10	--	New Release	JOHN

## 1 FEATURES

- (1) Display format : 320 × 240 dot-matrix, 1/240 duty.
- (2) Construction : STN LCD, Bezel, Heat Seal, Zebra, White Edge LED back-light, Touch Panel, Touch Panel driver (TSC2046) and PCB.
- (3) Display type : STN LCD, Negative type, 6 o'clock view.
- (4) Controller :RA8835
- (5) With temperature compensation circuit.
- (6) Power : +5V for logic circuit, Built-in DC/DC converter for LCD driving.
- (7) Normal temperature type.
- (8) ROHS compliant.

## 2 MECHANICAL DATA

Parameter	Stand Value	Unit
Dot size	0.345(W) × 0.345(H)	mm
Dot pitch	0.36(W) × 0.36(H)	mm
Viewing area	122.0(W) × 92.0(H)	mm
Module size (with Touch Panel)	160.0(W) × 109.0(H) × 12.5 max (T)	mm

## 3 ABSOLUTE MAXIMUM RATINGS

Parameter		Symbol	Min	Max	Unit
Logic Circuit Supply Voltage		VDD-VSS	-0.3	7.0	V
LCD Driving Voltage		VDD-VO	-0.3	26.0	V
Input Voltage		VI	-0.3	VDD+0.3	V
Extended temp. type	Operating Temp.	TOP	0	50	°C
	Storage Temp.	TSTG	-20	70	°C

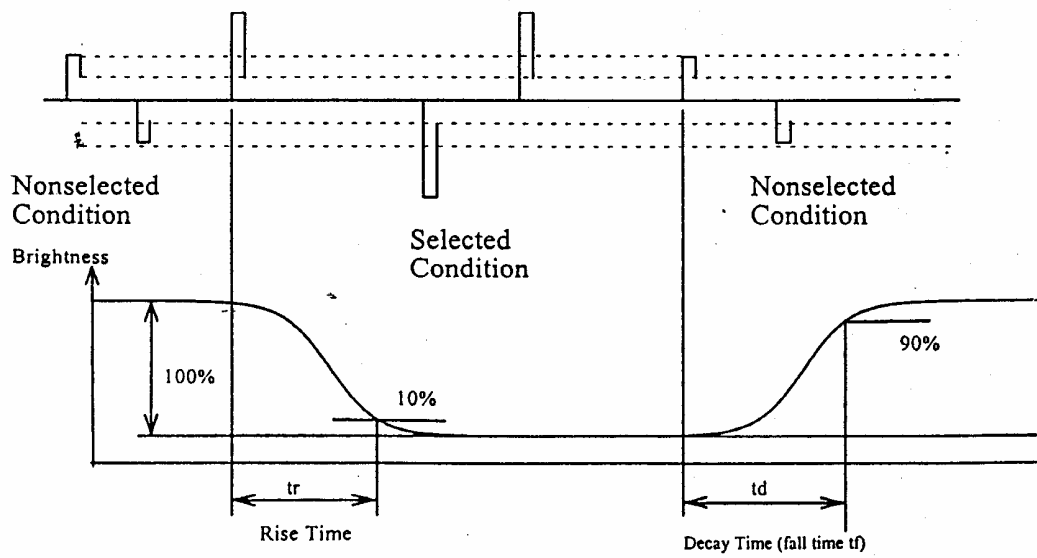
## 4 ELECTRO-OPTICAL CHARACTERISTICS

Parameter	Symbol	Condition	Min	Typ	Max	Unit	Note
----- Electronic Characteristics -----							
Logic Circuit Supply Voltage	VDD-VSS	--	--	5	5.5	V	
LCD Driving Voltage	VDD-VO	--	21.6	22.7	23.8	V	
Input Voltage	VIH	--	0.7 VDD	--	VDD	V	
	VIL	--	VSS	--	0.3 VDD	V	
Logic Supply Current	IDD	VDD = 5V	--	50	--	mA	
----- Optical Characteristics -----							
Contrast	CR	STN type	4	5.5	8		Note 1
Rise Time	tr	25°C	100	120	180	ms	Note 2
Fall Time	tf	25°C	110	140	210	ms	
Viewing Angle Range	θ f	25°C & CR≥2	--	30	--	Deg.	Note 3
	θ b		--	35	--		
	θ l		--	35	--		
	θ r		--	35	--		
Frame Frequency	FF	25°C	--	70	--	Hz	

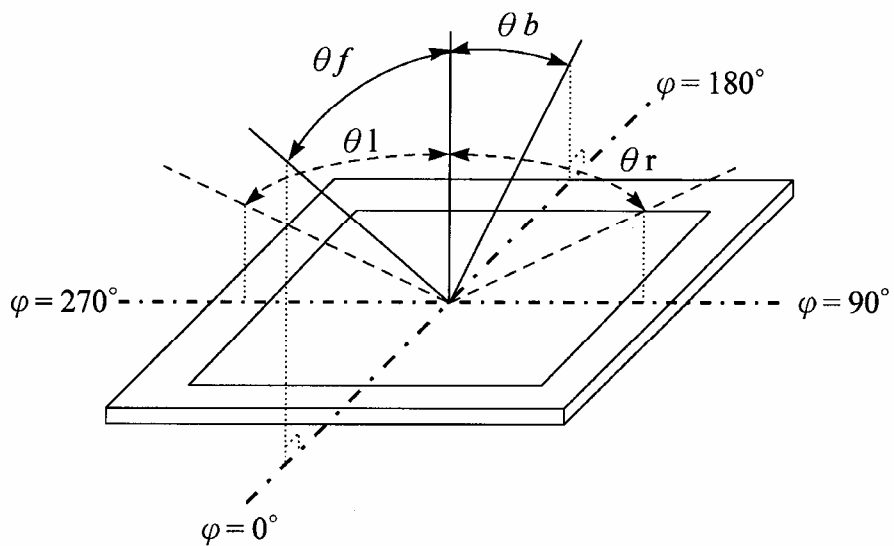
(NOTE 1) Contrast ratio :

CR = (Brightness in OFF state) / (Brightness in ON state)

( NOTE 2 ) Response time :



(NOTE 3) Viewing angle



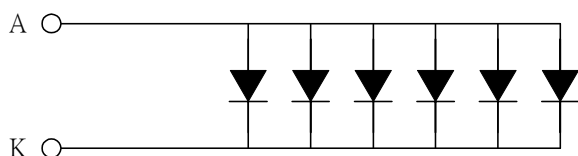
## 4.1 LED Back-light Electrical Specification

----- White LED Back-light Characteristics -----							
Parameter	Symbol	Condition	Min	Typ	Max	Unit	Note
Forward Current	IF	--	--	90	120	mA	Note 4
LCM Luminous intensity		IF=90mA	--	11	--	cd/m <sup>2</sup>	Note 4
Forward Voltage	VF	IF=90mA	--	3.2	3.5	V	Note 5
LED C.I.E	X	IF=90mA	0.28	0.31	0.34		Note 6
	Y	IF=90mA	0.29	0.32	0.35		

Note 4: Luminous intensity is decided by forward current of White LED.

Note 5: White LEDs are with voltage tolerance under the same current.

Note 6: White LEDs are with color tolerance under the same current.



\* LED Dice number = 6

## 4.2 Touch Panel Electrical Specification

Parameter	Condition	Standard Value
Terminal Resistance	X Axis	400 ~ 900 Ω
	Y Axis	200 ~ 500 Ω
Insulating Resistance	DC 25 V	More than 10MΩ
Linearity	--	±1.5 %
Notes life by Pen	Note a	100,000 times(min)
Input life by finger	Note b	1,000,000 times (min)

### Note A.

Notes area for pen notes life test is 10 x 9 mm.

Size of word is 7.5 x 6.72

Shape of pen end : R0.8

Load : 250 g

### Note B

By Silicon rubber tapping at same point

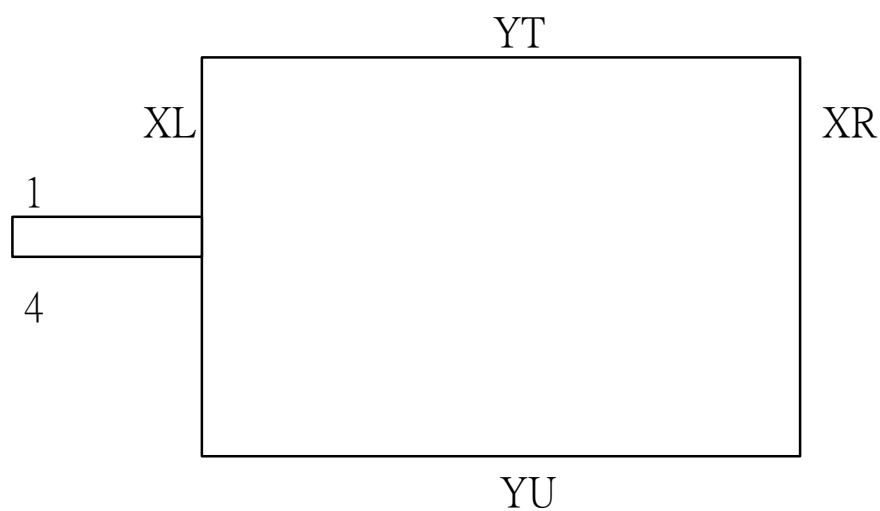
Shape of rubber end : R8

Load : 200g

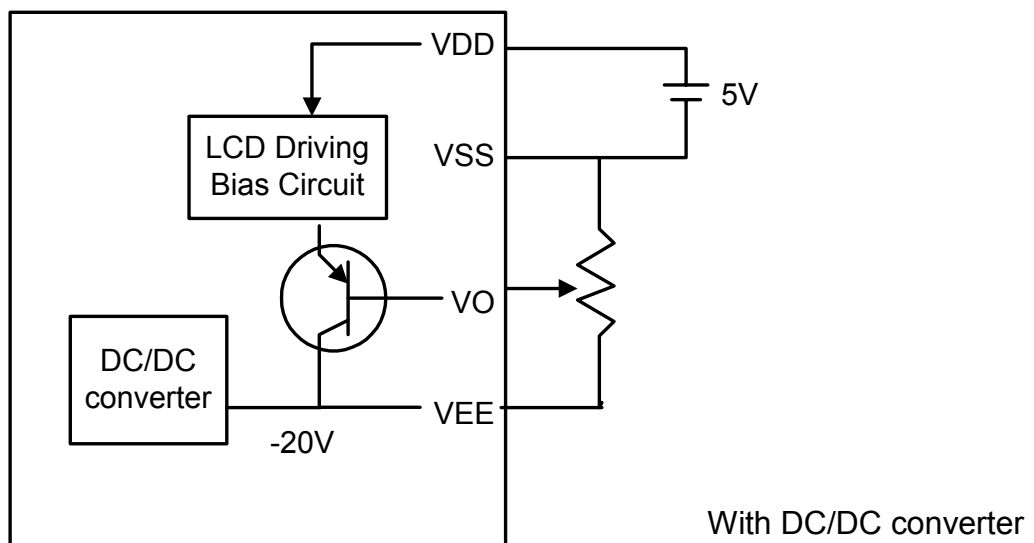
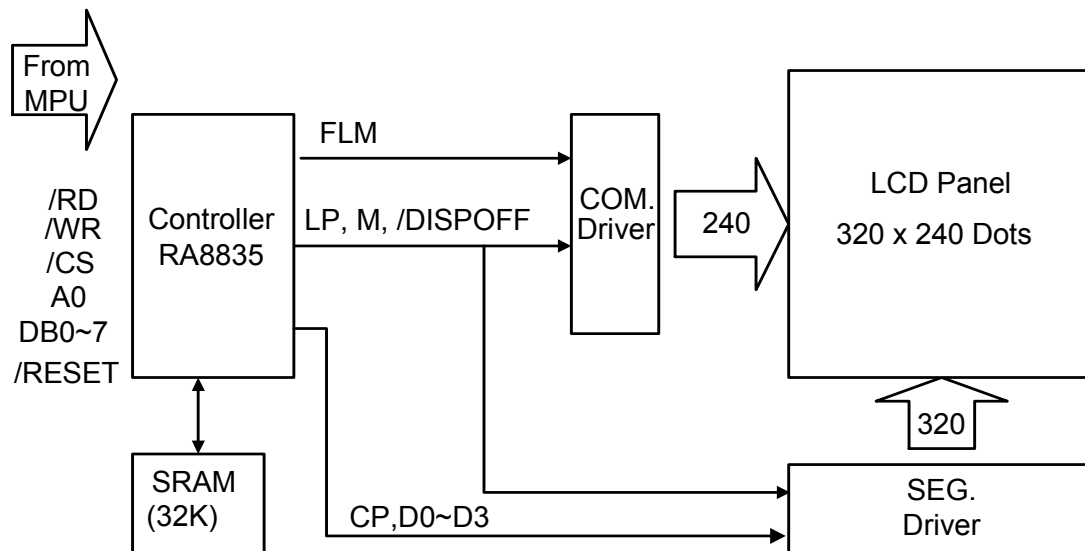
Frequency : 5 Hz

### Interface

No.	Symbol	Function
1	XR	Touch Panel Right Signal in X Axis
2	YT	Touch Panel Top Signal in Y Axis
3	XL	Touch Panel Left Signal in X Axis
4	YB	Touch Panel Bottom Signal in Y Axis



## 5 BLOCK DIAGRAM & POWER SUPPLY





## 6 PIN CONNECTIONS

CN2 : RA8835 Controller

PIN NO.	SIGNAL	LEVEL	FUNCTION
1	/RESET	H/L	Reset Signal
2	/RD	H/L	80 Series: Read Signal 68 Series: Enable Signal(E)
3	/WR	H/L	80 Series: Write Signal 68 Series: R/W Signal
4	/CS	H/L	Chip Select Signal
5	A0	H/L	Data Type Selection
6 ~ 13	DB0~DB7	H/L	Data Input(8 bits)
14	VDD	-	Power Supply for Logic(+5.0V)
15	VSS	-	Power Supply(Ground : 0V)
16	VEE	-	With DC/DC Negative voltage output (-20V)
17	VO	-	Contrast Adjustment Input
18*	SK	-	Serial Clock Touch Panel Left Signal in X Axis
19*	DO	-	Data Output Touch Panel Right Signal in X Axis
20*	DI	-	Data In Touch Panel Upper Signal in Y Axis
21*	CS	-	Chip Select Touch Panel Lower Signal in X Axis
22*	INT	-	Interrupt
23	NC	-	No connection
24	NC	-	No connection

18~22 : SK, DO, DI, CS, INT for Touch Panel controller TSC2046

## TIMING CHARACTERISTICS

### 6.1 8080 Family Interface Timing

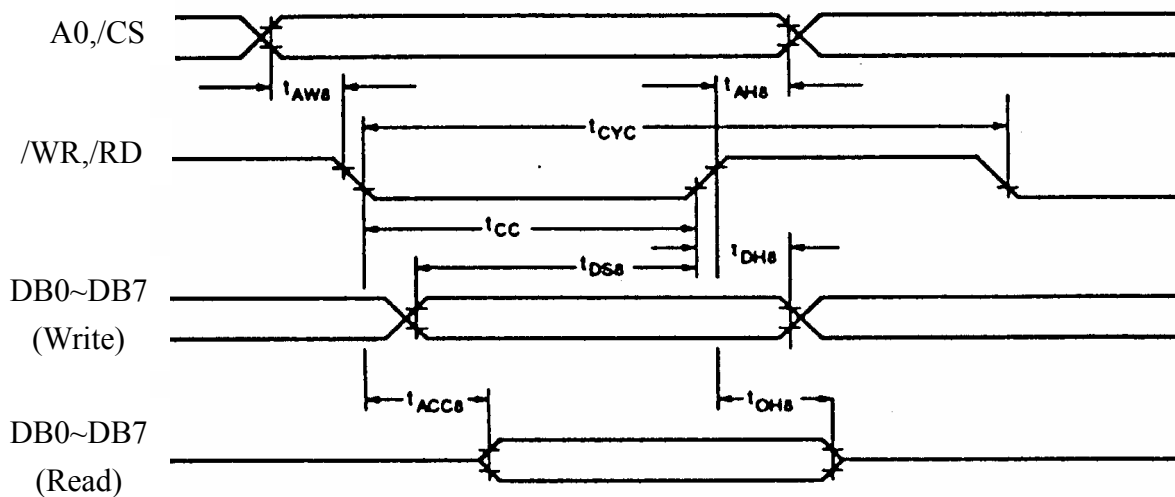
Parameter	Condition	Symbol	Min	Max	Unit	Remark
Address Hold Time	CL=100 pF VDD=2.7~4.5	tAH8	10		ns	A0,/CS
Address Setup Time		tAW8	0		ns	
System Cycle Time		tCYC	Note		ns	/WR,/RD
Strobe Pulse Width		tOC	150		ns	
Data Setup Time		tDS8	120		ns	DB0~DB7
Data Hold Time		tDH8	5		ns	
/RD Access Time		tACC8	-	80	ns	
Output Disable Time		tOH8	10	55	ns	

Note: For memory control and system control commands:

$$tCYC8 = 2tC + tOC + tCEA + 75 > tACV + 245$$

For all other commands:

$$tCYC8 = 4tC + tOC + 30$$



## 6.2 6800 Family Interface Timing

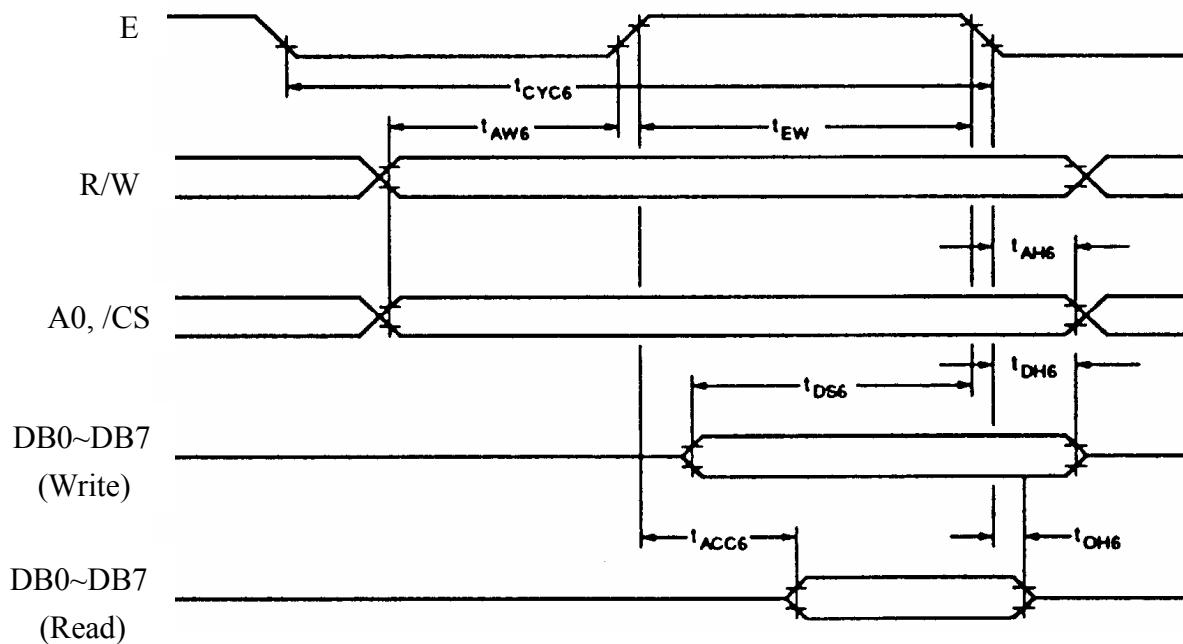
Parameter	Condition	Symbol	Min	Max	Unit	Remark
System Cycle Time	CL=100 pF VDD=2.7~4.5	tCYC6	Note		ns	A0,/CS, R/W
Address Setup Time		tAW6	10		ns	
Address Hold Time		tAH6	0		ns	
Data Setup Time		tDS6	120		ns	DB0~DB7
Data Hold Time		tDH6	0		ns	
Output Disable Time		tOH6	10	75	ns	
Access Time		tACC6	-	130	ns	
Enable Pulsewidth		tEW	150	-	ns	E

Note: For memory control and system control commands:

$$tCYC6 = 2tC + tEW + tCEA + 75 > tACV + 245$$

For all other commands:

$$tCYC6 = 4tC + tEW + 30$$



AC Electrical Characteristics

## 7 INSTRUCTION SET

Class	Command	Code											Hex	Command Description	Command read parameters	
		/RD	/WR	A0	D7	D6	D5	D4	D3	D2	D1	D0			Number of bytes	Section
System Control	SYSTEM SET	1	0	1	0	1	0	0	0	0	0	0	40	Initialized Device and display	8	8.2.1
	SLEEP IN	1	0	1	0	1	0	1	0	0	1	1	53	Enter Standby mode	0	8.2.2
Display Control	DISP ON/OFF	1	0	1	0	1	0	1	1	0	0	D	58, 59	Enable and disable display and display flashing	1	8.3.1
	SCROLL	1	0	1	0	1	0	0	0	1	0	0	44	set Display start address and display regions	10	8.3.2
	CSRFORM	1	0	1	0	1	0	1	1	1	0	1	5D	Set cursor byte	2	8.3.3
	CGRAM ADDR.	1	0	1	0	1	0	1	1	1	0	0	5C	Set start address of character generator RAM	2	8.3.6
	CSRDIR	1	0	1	0	1	0	0	1	1	CD 1	CD 0	4C to 4F	Set direction of cursor movement	0	8.3.4
	HDOT SCR	1	0	1	0	1	0	1	1		1	0	5A	set horizontal scroll position	1	8.3.7
	OVLAY	1	0	1	0	1	0	1	1	0	1	1	5B	set display overlay format	1	8.3.5
Drawing Control	CSRW	1	0	1	0	1	0	0	0	1	1	0	46	set cursor address	2	8.4.1
	CSRR	1	0	1	0	1	0	0	0	1	1	1	47	read cursor address	2	8.4.2
Memory Control	MWRITE	1	0	1	0	1	0	0	0	0	1	0	42	write to display memory	-	8.5.1
	MREAD	1	0	1	0	1	0	0	0	0	1	1	43	read from display memory	-	8.5.2

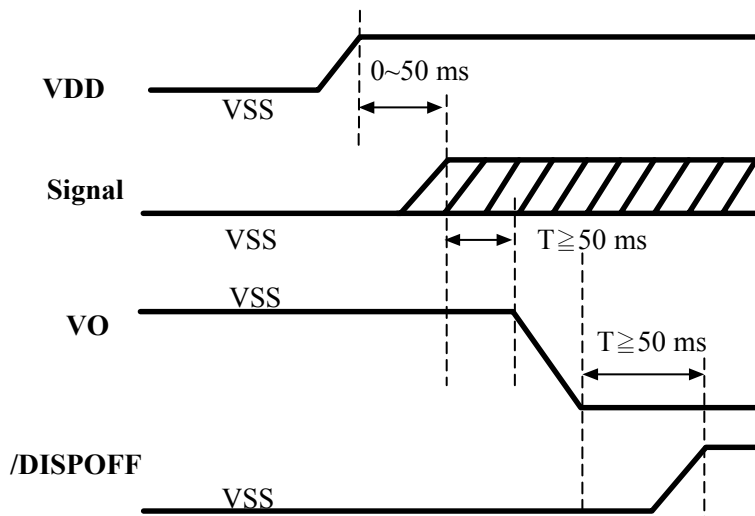
Note:

- In general, the internal registers of the RA8835 are modified as each command parameter is input. However, the microprocessor does not have to set all the parameters of a command and may send a new command before all parameters have been input. The internal registers for the parameters that have been input will have been changed but the remaining parameter registers are unchanged.
- 2 bytes parameters( where two bytes are treated as 1 data item) are handled as following:
  - CSRW, CSRR: Each byte is processed individually. The microprocessor may read or write just the low byte of the cursor address.
  - SYSTEM SET, SCROLL, CGRAM ADR. : Both parameter bytes are processed together. If the command is changed after half of the parameter has been input, the single byte is ignored.
- APL and APH are 2-byte parameters, but are treated as two 1-byte parameters.
- Please refer to RA8835 LCD Controller Data Book for detail.

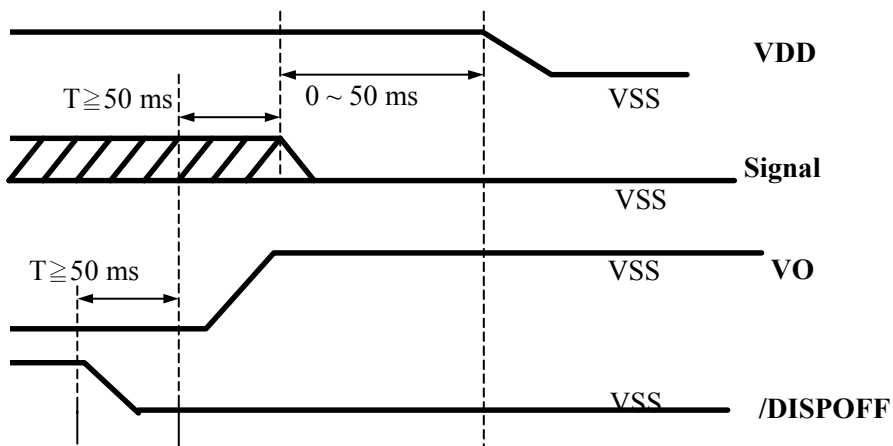
## 7.1 Power ON/OFF Sequence

Please maintain the blow sequence when turning on and off the power supply of the module. If /DISPOFF is supplied to the module while internal alter signal for LCD driving (M) is unstable, DC component will be supplied to the LCD panel. This may cause damage the LCD module.

### POWER ON SEQUENCE



### POWER OFF SEQUENCE



## 8 JUMPER SETTING

Item	Option	Jumper Setting	Remark
MPU	80 family (default)	Pin 1,2 short on JP6	
	68 family	Pin 2,3 short on JP6	

## 9 QUALITY AND RELIABILITY

### 9.1 TEST CONDITIONS

Tests should be conducted under the following conditions :

Ambient temperature :  $25 \pm 5^{\circ}\text{C}$

Humidity :  $60 \pm 25\% \text{ RH}$ .

### 9.2 SAMPLING PLAN

Sampling method shall be in accordance with MIL-STD-105E , level II, normal single sampling plan .

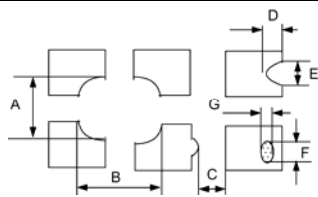
### 9.3 ACCEPTABLE QUALITY LEVEL


A major defect is defined as one that could cause failure to or materially reduce the usability of the unit for its intended purpose. A minor defect is one that does not materially reduce the usability of the unit for its intended purpose or is an infringement from established standards and has no significant bearing on its effective use or operation.

### 9.4 APPEARANCE

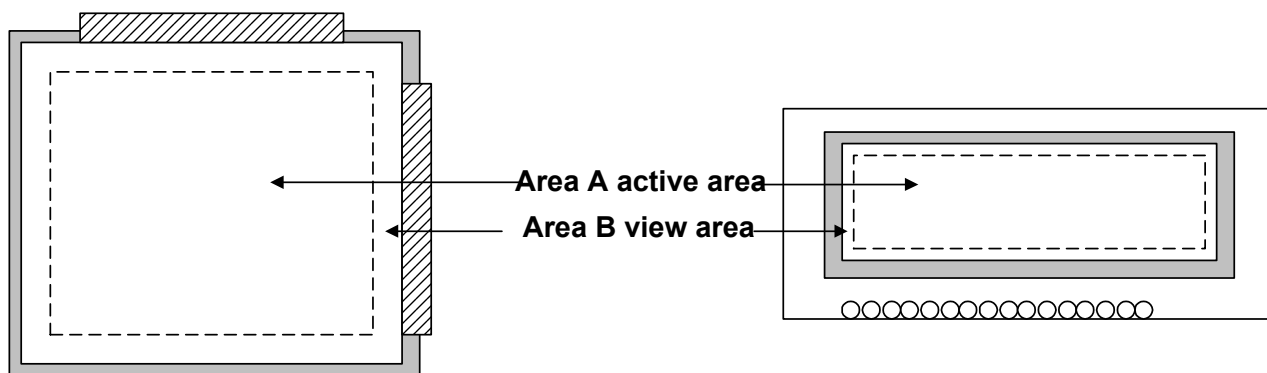
An appearance test should be conducted by human sight at approximately 30 cm distance from the LCD module under fluorescent light. The inspection area of LCD panel shall be within the range of following limits.

## 9.5 INSPECTION QUALITY CRITERIA

No.	Item	Criterion for defects	Defect type															
1	Non display	No non display is allowed	Major															
2	Irregular operation	No irregular operation is allowed	Major															
3	Short	No short are allowed	Major															
4	Open	Any segments or common patterns that don't activate are rejectable.	Major															
5	Black/White spot	<table><tr><th>Size D (mm)</th><th>Acceptable number</th></tr><tr><td><math>D \leq 0.15</math></td><td>Ignore</td></tr><tr><td><math>0.15 &lt; D \leq 0.20</math></td><td>3</td></tr><tr><td><math>0.20 &lt; D \leq 0.30</math></td><td>2</td></tr><tr><td><math>0.30 &lt; D</math></td><td>0</td></tr></table>	Size D (mm)	Acceptable number	$D \leq 0.15$	Ignore	$0.15 < D \leq 0.20$	3	$0.20 < D \leq 0.30$	2	$0.30 < D$	0	Minor					
Size D (mm)	Acceptable number																	
$D \leq 0.15$	Ignore																	
$0.15 < D \leq 0.20$	3																	
$0.20 < D \leq 0.30$	2																	
$0.30 < D$	0																	
6	Black/White line	<table><tr><th>Length(mm)</th><th>Width (mm)</th><th>Acceptable number</th></tr><tr><td><math>10 &lt; L</math></td><td><math>0.03 &lt; W \leq 0.04</math></td><td>5</td></tr><tr><td><math>5.0 &lt; L \leq 10</math></td><td><math>0.04 &lt; W \leq 0.06</math></td><td>3</td></tr><tr><td><math>1.0 &lt; L \leq 5.0</math></td><td><math>0.06 &lt; W \leq 0.07</math></td><td>2</td></tr><tr><td><math>L \leq 1.0</math></td><td><math>0.07 &lt; W \leq 0.09</math></td><td>1</td></tr></table>	Length(mm)	Width (mm)	Acceptable number	$10 < L$	$0.03 < W \leq 0.04$	5	$5.0 < L \leq 10$	$0.04 < W \leq 0.06$	3	$1.0 < L \leq 5.0$	$0.06 < W \leq 0.07$	2	$L \leq 1.0$	$0.07 < W \leq 0.09$	1	Minor
Length(mm)	Width (mm)	Acceptable number																
$10 < L$	$0.03 < W \leq 0.04$	5																
$5.0 < L \leq 10$	$0.04 < W \leq 0.06$	3																
$1.0 < L \leq 5.0$	$0.06 < W \leq 0.07$	2																
$L \leq 1.0$	$0.07 < W \leq 0.09$	1																
7	Back Light	1. No Lighting is rejectable 2. Flickering and abnormal lighting are rejectable	Major															
8	dot defect	<table><tr><td>Bright dot</td><td><math>N \leq 1</math></td></tr><tr><td>Dark dot</td><td><math>N \leq 3</math></td></tr><tr><td>Total dot defect (Bright dot + Dark dot)</td><td><math>N \leq 3</math></td></tr><tr><td>Minimum distance between dark dot and dark dot</td><td><math>L \geq 5 \text{ mm}</math></td></tr></table>	Bright dot	$N \leq 1$	Dark dot	$N \leq 3$	Total dot defect (Bright dot + Dark dot)	$N \leq 3$	Minimum distance between dark dot and dark dot	$L \geq 5 \text{ mm}$	Minor							
Bright dot	$N \leq 1$																	
Dark dot	$N \leq 3$																	
Total dot defect (Bright dot + Dark dot)	$N \leq 3$																	
Minimum distance between dark dot and dark dot	$L \geq 5 \text{ mm}$																	
9	Display pattern	<div></div> <div>Unit:mm</div> <table><tr><td><math>\frac{A+B}{2} \leq 0.30</math></td><td><math>0 &lt; C</math></td><td><math>\frac{D+E}{2} \leq 0.25</math></td><td><math>\frac{F+G}{2} \leq 0.25</math></td></tr></table> <div>Note: 1. Acceptable up to 3 damages 2. NG if there're to two or more pinholes per dot</div>	$\frac{A+B}{2} \leq 0.30$	$0 < C$	$\frac{D+E}{2} \leq 0.25$	$\frac{F+G}{2} \leq 0.25$	Minor											
$\frac{A+B}{2} \leq 0.30$	$0 < C$	$\frac{D+E}{2} \leq 0.25$	$\frac{F+G}{2} \leq 0.25$															

10	Blemish & Foreign matters  Size: $D = \frac{A+B}{2}$	Size D (mm)			Acceptable number	Minor	
		$D \leq 0.15$			Ignore		
		$0.15 < D \leq 0.20$			3		
		$0.20 < D \leq 0.30$			2		
$0.30 < D$			0				
11	Scratch on Polarizer 	Width (mm)			Length (mm)	Acceptable number	Minor
		$W \leq 0.03$			Ignore	Ignore	
		$0.03 < W \leq 0.05$			$L \leq 2.0$	Ignore	
		$0.05 < W \leq 0.08$			$L > 2.0$	1	
$0.08 < W$			$L > 1.0$	1			
			$L \leq 1.0$	Ignore			
			Note (1)	Note(1)			
Note(1) Regard as a blemish							
12	Bubble in polarizer	Size D (mm)			Acceptable number	Minor	
		$D \leq 0.20$			Ignore		
		$0.20 < D \leq 0.50$			3		
		$0.50 < D \leq 0.80$			2		
$0.80 < D$			0				
13	Stains on LCD panel surface	Stains that cannot be removed even when wiped lightly with a soft cloth or similar cleaning too are rejectable.				Minor	
14	Rust in Bezel	Rust which is visible in the bezel is rejectable.				Minor	
15	Defect of land surface contact (poor soldering)	Evident crevices which is visible are rejectable.				Minor	
16	Parts mounting	1. Failure to mount parts 2. Parts not in the specifications are mounted 3. Polarity, for example, is reversed				Major Major Major	
17	Parts alignment	1. LSI, IC lead width is more than 50% beyond pad outline.				Minor	
		2. Chip component is off center and more than 50% of the leads is off the pad outline.				Minor	
18	Conductive foreign matter (Solder ball, Solder chips)	1. $0.45 < \varphi$ , $N \geq 1$				Major	
		2. $0.30 < \varphi \leq 0.45$ , $N \geq 1$ $\varphi$ :Average diameter of solder ball (unit: mm)				Minor	
		3. $0.50 < L$ , $N \geq 1$ L: Average length of solder chip (unit: mm)				Minor	
19	Faulty PCB correction	1. Due to PCB copper foil pattern burnout, the pattern is connected, using a jumper wire for repair; 2 or more places are corrected per PCB.				Minor	
		2. Short circuited part is cut, and no resist coating has been performed.				Minor	





## 9.6 RELIABILITY

Test Item	Test Conditions	Note
	Normal Temp. type	
High Temperature Operation	50±3°C , t=96 hrs	
Low Temperature Operation	0±3°C , t=96 hrs	
High Temperature Storage	70±3°C , t=96 hrs	1,2
Low Temperature Storage	-20±3°C , t=96 hrs	1,2
Temperature Cycle	-20°C ~ 25°C ~ 70°C 30 min. 5 min. 30 min. ( 1 cycle ) Total 5 cycle	1,2
Humidity Test	40 °C, Humidity 90%, 96 hrs	1,2
Vibration Test (Packing)	Sweep frequency : 10 ~ 55 ~ 10 Hz/1min Amplitude : 0.75mm Test direction : X.Y.Z/3 axis Duration : 30min/each axis	2

Note 1 : Condensation of water is not permitted on the module.

Note 2 : The module should be inspected after 1 hour storage in normal conditions (15-35°C , 45-65%RH).

Definitions of life end point :

- Current drain should be smaller than the specific value.
- Function of the module should be maintained.
- Appearance and display quality should not have degraded noticeably.
- Contrast ratio should be greater than 50% of the initial value.

## **10 HANDLING PRECAUTIONS**

- (1) A LCD module is a fragile item and should not be subjected to strong mechanical shocks.
- (2) Avoid applying pressure to the module surface. This will distort the glass and cause a change in color.
- (3) Under no circumstances should the position of the bezel tabs or their shape be modified.
- (4) Do not modify the display PCB in either shape or positioning of components.
- (5) Do not modify or move location of the zebra or heat seal connectors.
- (6) The device should only be soldered to during interfacing. Modification to other areas of the board should not be carried out.
- (7) In the event of LCD breakage and resultant leakage of fluid do not inhale, ingest or make contact with the skin. If contact is made rinse immediately.
- (8) When cleaning the module use a soft damp cloth with a mild solvent, such as Isopropyl or Ethyl alcohol. The use of water, ketone or aromatic is not permitted.
- (9) Prior to initial power up input signals should not be applied.
- (10) Protect the module against static electricity and observe appropriate anti-static precautions.

## 11 OUTLINE DIMENSION

