



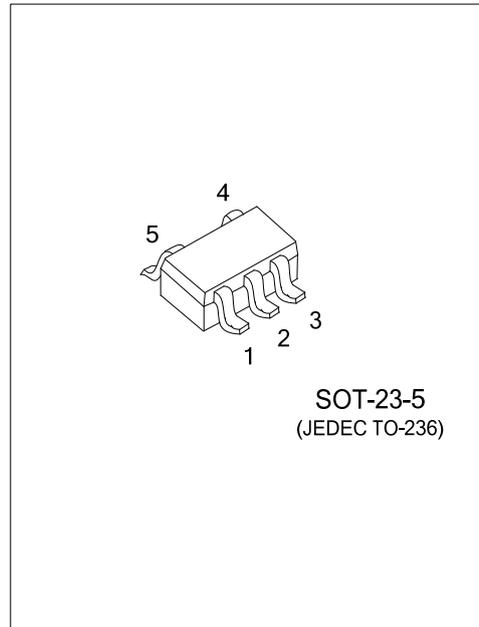
150mA CMOS LDO WITH SHUTDOWN AND REFERENCE BYPASS

DESCRIPTION

The UTC **LR1185**, a 150mA LDO regulator has very high PSRR and super low dropout voltage especially suitable for wireless and portable applications.

In the field of hand-held wireless devices, board space and battery life are the main concerns of designers and end-users. Because of the low quiescent current and low ESR ceramic capacitors, UTC **LR1185** can satisfy those concerns.

Furthermore, low current consumption (50µA), high output accuracy, current limiting protection, and high ripple rejection ratio are advantages of UTC **LR1185**.



FEATURES

- * Operating voltage ranges : 2.7V~5.5V
- * Dropout : 100mV at 150mA
- * When IC shutdown: 5mA discharge current of V_{OUT}
- * Extreme low Noise for DSC application
- * Extreme fast response in line/load transient
- * Internal current limiting protection
- * Internal thermal shutdown protection
- * High PSRR
- * Recommended 1µF output capacitor only for stability
- * With TTL logic controlled shutdown input

ORDERING INFORMATION

Ordering Number		Package Packing	
Lead Free	Halogen Free		
LR1185L-xx-AE5-R	LR1185G-xx-AE5-R	SOT-23-5	Tape Reel

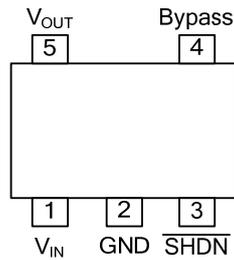
Note: xx : Output Voltage, refer to Marking Information.

<p>LR1185L-xx-AE5-R</p> <ul style="list-style-type: none"> (1)Packing Type (2)Package Type (3)Output Voltage Code (4)Lead Free 	<ul style="list-style-type: none"> (1) R: Tape Reel (2) AE5: SOT-23-5 (3) xx: refer to Marking Information (4) L: Lead Free, G: Halogen Free
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MARKING INFORMATION

PACKAGE	VOLTAGE CODE	MARKING
SOT-23-5	4B: 4.15V	

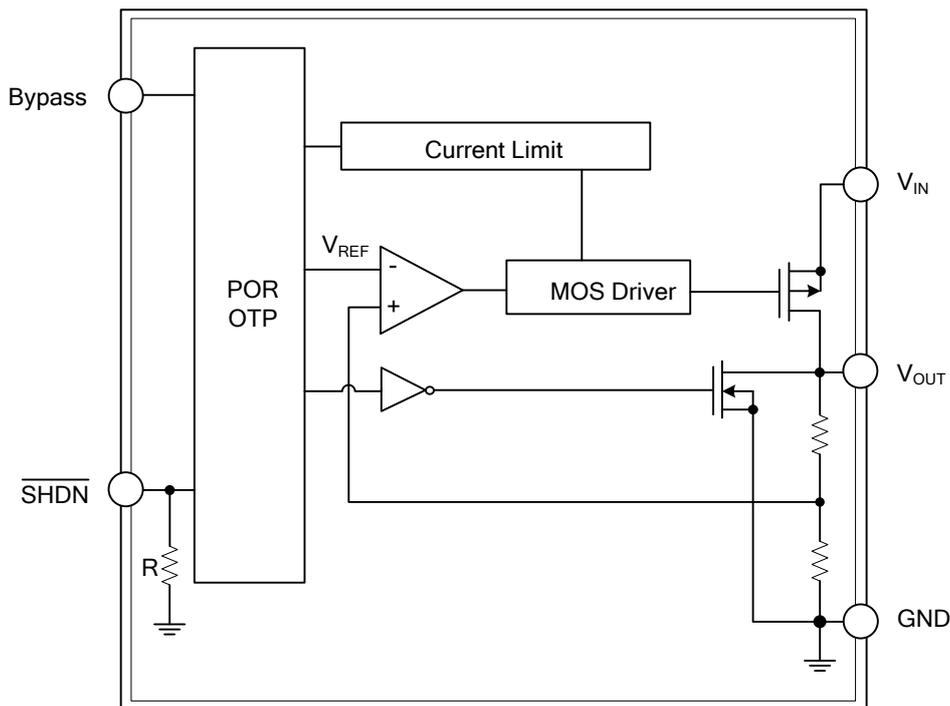
PIN CONFIGURATION



PIN DESCRIPTION

PIN NO.	PIN NAME	DESCRIPTION
1	V_{IN}	Unregulated supply input.
2	GND	Ground terminal.
3	SHDN	Shutdown control input.
4	Bypass	Reference bypass input. Connecting a 470pF to this input further reduces output noise.
5	V_{OUT}	Regulated voltage output.

BLOCK DIAGRAM



■ ABSOLUTE MAXIMUM RATING ($T_A=25^\circ\text{C}$, unless otherwise specified.)

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Input Voltage	V_{IN}	6	V
Output Voltage	V_{OUT}	$-0.3\sim V_{IN}+0.3$	V
Power Dissipation ($T_A=25^\circ\text{C}$)	P_D	0.38	W
Junction Temperature	T_J	150	$^\circ\text{C}$
Storage Temperature	T_{STG}	$-65\sim+150$	$^\circ\text{C}$

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	θ_{JA}	256	$^\circ\text{C}/\text{W}$

■ OPERATING CONDITIONS

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Input Voltage	V_{IN}	2.7~5.5	V
Junction Temperature	T_J	$-40\sim+125$	$^\circ\text{C}$
Ambient Temperature	T_A	$-40\sim+85$	$^\circ\text{C}$

■ ELECTRICAL CHARACTERISTICS

($V_{IN}=V_{OUT}+0.5\text{V}$, $V_{EN}=V_{IN}$, $C_{IN}=C_{OUT}=1\mu\text{F}$ (Ceramic), $T_A=25^\circ\text{C}$, unless otherwise specified.)

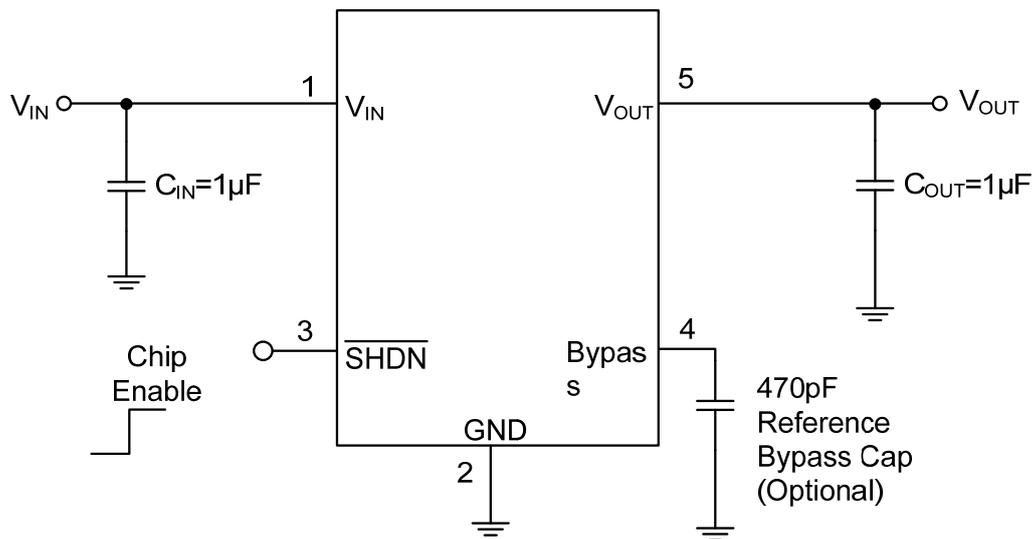
PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Input Voltage	V_{IN}		2.7	5.5		V
Output Voltage Accuracy	ΔV_{OUT}	$I_{OUT}=10\text{mA}$	-2	0	+2	%
Line Regulation	$\frac{\Delta V_{OUT}}{\Delta V_{IN} \times V_{OUT}}$	$V_{IN}=(V_{OUT}+0.5\text{V})\sim 5.5\text{V}$, $I_{OUT}=1\text{mA}$		0.01	0.2	%/V
Load Regulation (Note 1)	$\frac{\Delta V_{OUT}}{V_{OUT}}$	$1\text{mA}<I_{OUT}<150\text{mA}$, $2.7\text{V}\leq V_{IN}\leq 5.5\text{V}$		0.5	1	%
Quiescent Current (Note 2)	I_Q	$V_{EN}=5\text{V}$, $I_{OUT}=0\text{mA}$		25	50	μA
Standby Current	I_{STN-BY}	$V_{EN}=0\text{V}$			1	μA
Current Limit	I_{LIMIT}	$R_{LOAD}=0\Omega$, $2.7\text{V}\leq V_{IN}\leq 5.5\text{V}$		0.3	0.5	A
Dropout Voltage (Note 3)	V_D	$I_{OUT}=150\text{mA}$		100	200	mV
Soft Start Time		$V_{OUT}=2.5\text{V}$, $C_{SS}=1\text{nF}$, $C_{OUT}=1\mu\text{F}$		0.7	1.2	ms
EN Threshold	Logic-Low	V_{IL}			0.6	V
	Logic-High	V_{IH}	1.6			V
Enable Pin Current	I_{EN}		0.1	1	5	μA
Over Temperature Shutdown	OTS			170		$^\circ\text{C}$
Over Temperature Hysteresis	OTH			30		$^\circ\text{C}$
Power Supply Rejection Rate	PSRR	$I_{OUT}=10\text{mA}$, $f=10\text{kHz}$		55		dB
Output Noise Voltage	e_N	$V_{OUT}=1.5\text{V}$, $C_{OUT}=1\mu\text{F}$, $I_{OUT}=0\text{mA}$, $C_{SS}=1\text{nF}$		40		μV_{RMS}

Notes: 1. Regulation is measured at constant junction temperature by using a 2ms current pulse. Devices are tested for load regulation in the load range from 1mA to 500mA.

2. Quiescent, or ground current, is the difference between input and output currents. It is defined by $I_Q=I_{IN}-I_{OUT}$ under no load condition ($I_{OUT}=0\text{mA}$). The total current drawn from the supply is the sum of the load current plus the ground pin current.

3. The dropout voltage is defined as $V_{IN}-V_{OUT}$, which is measured when V_{OUT} is $V_{OUT(NORMAL)}\times 98\%$.

■ TYPICAL APPLICATION CIRCUIT



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