

## 300mA, Micropower, VLDO Linear Regulator

### UM175XX SOT23-5

#### General Description

The UM175XX series are VLDO (very low dropout) linear regulators designed for low power portable applications. Typical output noise is only  $75\mu\text{V}_{\text{RMS}}$  and maximum dropout is just 90mV at the load current of 150mA. The internal P-channel MOSFET pass transistor requires no base current, allowing the device to draw only 100 $\mu\text{A}$  during normal operation at the maximum load current of 300mA.

Other features include high output voltage accuracy, excellent transient response, under voltage lockout, stability with ultralow ESR ceramic capacitors as small as 1 $\mu\text{F}$ , short-circuit and thermal overload protection and output current limiting.

The UM175XX series are available in a low profile SOT23-5 package.

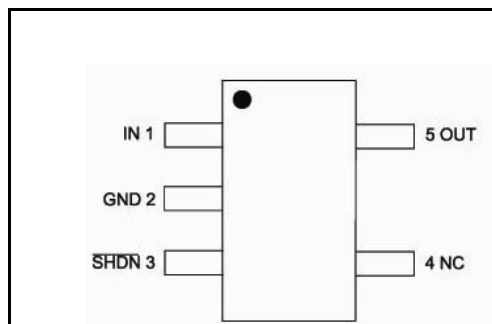
#### Applications

- Bluetooth/802.11 Cards
- PDAs and Notebook Computers
- Portable Instruments and Battery-Powered Systems
- Cellular Phones

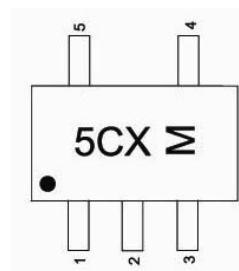
#### Features

- Very Low Dropout: 90mV(max) at 150mA
- Maximum Input Voltage: 6.0V
- Low Noise:  $75\mu\text{V}_{\text{RMS}}$  (10Hz to 100kHz)
- $\pm 2\%$  Voltage Accuracy at 150mA
- Fast Transient Response
- Under Voltage Lockout
- Fixed Output Voltage: 3.3V/3.0V/2.8V/2.7V/2.5V/1.8V/1.5V/1.3V/1.2V
- Output Current Limit
- No Protection Diodes Needed
- Stable with 1 $\mu\text{F}$  Output Capacitor
- Short-Circuit and Thermal Overload Protection
- Low Profile SOT23-5 Package

#### Pin Configurations



#### Top View



UM175XX  
M: Monthly Code  
SOT23-5

## Ordering Information

Part Number	Output Voltage	Packaging Type	Marking Code	Shipping Qty
UM17533	3.3V	SOT23-5	5CU	3000pcs/7Inch Tape & Reel
UM17530	3.0V		5CR	
UM17528	2.8V		5CQ	
UM17527	2.7V		5CP	
UM17525	2.5V		5CN	
UM17518	1.8V		5CK	
UM17515	1.5V		5CJ	
UM17513	1.3V		5CF	
UM17512	1.2V		5CE	

## Pin Description

Pin Number	Symbol	Function
1	IN	Power Supply
2	GND	Ground
3	$\overline{\text{SHDN}}$	Shutdown Input, Active Low
4	NC	Not Connected
5	OUT	Voltage Regulated Output

## Absolute Maximum Ratings (Note 1)

Symbol	Parameter	Value	Unit
$V_{\text{IN}}$	Supply Voltage on IN Pin	-7.5 to +7.5	V
$V_{\overline{\text{SHDN}}}$	Voltage on $\overline{\text{SHDN}}$ Pin	-0.3 to +7.5	V
$V_{\text{OUT}}$	Voltage on OUT Pin	-0.3 to +7.5	V
	Output Short-Circuit Duration	Indefinite	
$T_{\text{J}}$	Operating Junction Temperature (Notes 2, 3)	-40 to +125	°C
$T_{\text{STG}}$	Storage Temperature Range	-65 to +150	°C
$T_{\text{L}}$	Lead Temperature for Soldering 10 seconds	+300	°C

Note 1: Absolute Maximum Ratings are those values beyond which the life of a device may be impaired.

Note 2: The UM175XX is tested and specified under pulse load conditions such that  $T_{\text{J}} \approx T_{\text{A}}$ . The device is guaranteed to meet performance specifications from 0 °C to 70 °C . Specifications over the – 40 °C to 125 °C operating junction temperature range are assured by design, characterization and correlation with statistical process controls.

Note 3: This IC includes overtemperature protection that is intended to protect the device during momentary overload conditions. Junction temperature will exceed 125 °C when overtemperature protection is active. Continuous operation above the specified maximum operating junction temperature may impair device reliability.

## Electrical Characteristics

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
$V_{IN}$	Input Voltage Range		$V_{OUT}+$ $V_{DROP}$		6.0	V
$V_{UVLO1}$ (Note1)	Input Under Voltage Lockout	$V_{IN}$ falling	2.0		2.6	V
$V_{UVLO2}$ (Note2)	Input Under Voltage Lockout	$V_{IN}$ falling	2.1		2.3	V
$I_Q$	Operating Quiescent Current	$I_{OUT}=0mA$		90		$\mu A$
		$I_{OUT}=300mA$		100		
$I_{SHDN}$	Shutdown Leakage Current				1	$\mu A$
	ESD Rating	Human Body Mode	2			KV
$I_{OUT}$	Output Current		300			mA
	Output Voltage Accuracy	$1mA \leq I_{OUT} \leq 150mA$ , $T_A = +25^\circ C$	-1		+1	%
		$1mA \leq I_{OUT} \leq 150mA$ , $T_A = -40^\circ C$ to $+85^\circ C$	-2		+2	
		$1mA \leq I_{OUT} \leq 300mA$ , $T_A = -40^\circ C$ to $+85^\circ C$	-2.5		+2.5	
$\Delta V_{DO}$	Dropout Voltage	$I_{OUT}=150mA$			90	mV
$I_{LIMIT}$	Output Current Limit	$V_{IN} \geq 2.5V$	450			mA
t	Startup Time Response	$R_L=68\Omega$ , $C_{OUT}=1\mu F$		20		$\mu s$
$V_{IL}$	$\overline{SHDN}$ Input Low Voltage				$0.3 \times V_{IN}$	V
$V_{IH}$	$\overline{SHDN}$ Input High Voltage		$0.7 \times V_{IN}$			V
	$\overline{SHDN}$ Input Current	$\overline{SHDN}=V_{IN}$ or GND	-1	0.1	+1	$\mu A$
$T_{SHDN}$	Thermal-Shutdown Temperature			160		$^\circ C$
$\Delta T_{SHDN}$	Thermal-Shutdown Hysteresis			20		$^\circ C$
	Line Regulation	$V_{OUT}+1V \leq V_{IN} \leq V_{OUT}+2V$ $I_{OUT}=10mA$		0.09		%/V
	Load Regulation	$V_{IN}=V_{OUT}+1V$ $1mA \leq I_{OUT} \leq 150mA$		0.2		%
	Output Voltage Noise	10Hz to 100KHz $C_{IN}=0.1\mu F$ , $I_{OUT}=10mA$		75		$\mu V_{RMS}$
PSRR	Power Supply Ripple Rejection	$V_{IN}=V_{OUT}+1V$ $I_{OUT}=100mA$	F=100Hz	70		dB
			F=1KHz	65		
			F=10KHz	50		
			F=100KHz	40		

Note1:  $V_{UVLO1}$  is measured for devices with  $V_{OUT} \geq 1.8V$ .

Note2:  $V_{UVLO2}$  is measured for devices with  $V_{OUT} \leq 1.5V$ .

## Pin Function

**IN (Pin 1):** Power for UM175XX and Load. Power is supplied to the devices through the IN pin. The IN pin should be locally bypassed to ground if the UM175XX series are more than a few inches away from another source of bulk capacitance. In general, the output impedance of a battery rises with frequency, so it is usually advisable to include an input bypass capacitor in battery-powered circuits. A capacitor in the range of  $0.1\mu\text{F}$  to  $1\mu\text{F}$  is usually sufficient. The UM175XX series are designed to withstand reverse voltages on the IN pin with respect to both ground and the output pin. In the case of a reversed input, which can happen if a battery is plugged in backwards, the UM175XX will act as if there is a large resistor in series with its input with only a small amount of current flow.

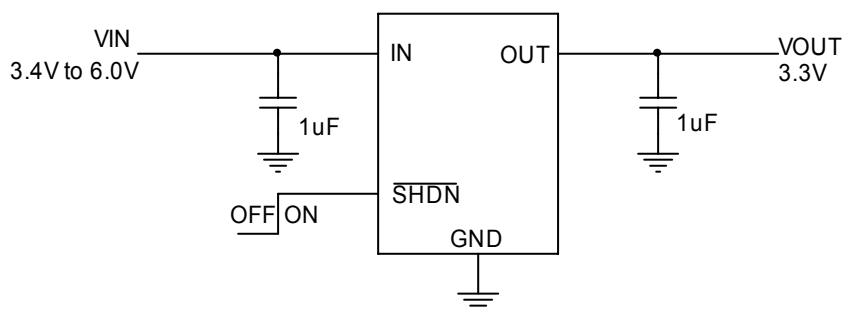
**GND (Pin 2):** Ground and Heat Sink. Solder to a ground plane or large pad to maximize heat dissipation.

**$\overline{\text{SHDN}}$  (Pin 3):** Shutdown, Active Low. This pin is used to put the UM175XX into shutdown. The  $\overline{\text{SHDN}}$  pin cannot be left floating and must be tied to the input pin if not used.

**NC (Pin 4):** Not Connected.

**OUT (Pin 5):** Voltage Regulated Output. The OUT pin supplies power to the load. A minimum output capacitor of  $1\mu\text{F}$  is required to ensure stability. Larger output capacitors may be required for applications with large transient loads to limit peak voltage transients. See the Applications Information section for more information on output capacitance.

## Typical Application Circuit



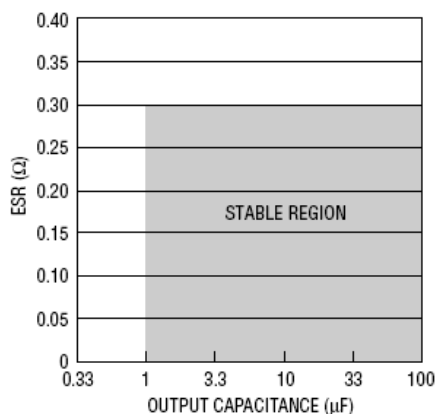
## Applications Information

The UM175XX series are 300mA very low dropout regulators with micropower quiescent current and shutdown. The maximum dropout is only 90mV at the load current of 150mA. Output voltage noise is as low as  $75\mu\text{V}_{\text{RMS}}$  over a 10Hz to 100kHz bandwidth.

In addition to the low quiescent current, the UM175XX regulators incorporate several protection features which make them ideal for use in battery-powered systems. The devices also include current limit and thermal overload protection, and will survive an output short circuit indefinitely. The fast transient response over comes the traditional tradeoff between dropout voltage, quiescent current and load transient response inherent in most regulators by using a proprietary new architecture

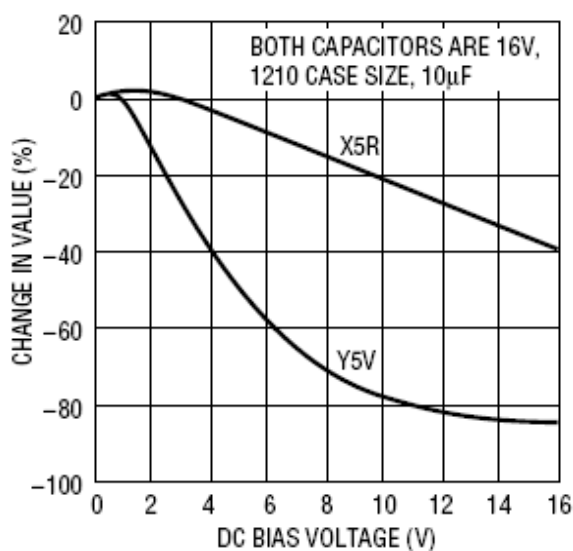
### Output Capacitance and Transient Response

The UM175XX regulators are designed to be stable with a wide range of output capacitors. The ESR of the output capacitor affects stability, most notably with small capacitors. A minimum output capacitor of  $1\mu\text{F}$  with an ESR of  $0.3\Omega$  or less is recommended to ensure stability. The UM175XX series are micropower devices and output transient response will be a function of output capacitance. Larger values of output capacitance decrease the peak deviations and provide improved transient response for larger load current changes. The shaded region of Figure 1 defines the region over which the UM175XX regulators are stable. The maximum ESR allowed is  $0.3\Omega$ . High ESR tantalum and electrolytic capacitors may be used, but a low ESR ceramic capacitor must be in parallel at the output. There is no minimum ESR requirement.

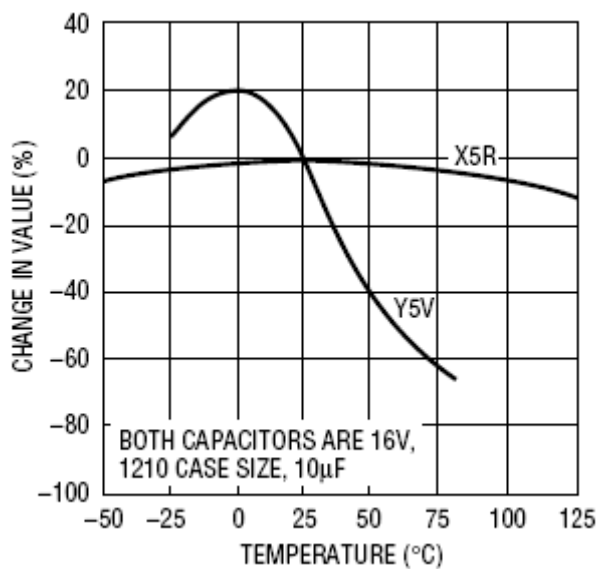


**Figure 1**

Extra consideration must be given to the use of ceramic capacitors. Ceramic capacitors are manufactured with a variety of dielectrics, each with different behavior across temperature and applied voltage. The most common dielectrics used are Z5U, Y5V, X5R and X7R. The Z5U and Y5V dielectrics are good for providing high capacitances in a small package, but exhibit strong voltage and temperature coefficients as shown in Figures 2 and 3. When used with a 5V regulator, a  $10\mu\text{F}$  Y5V capacitor can exhibit an effective value as low as  $1\mu\text{F}$  to  $2\mu\text{F}$  over the operating temperature range. The X5R and X7R dielectrics result in more stable characteristics and are more suitable for use as the output capacitor. The X7R type has better stability across temperature, while the X5R is less expensive and is available in higher values.



**Figure 2**



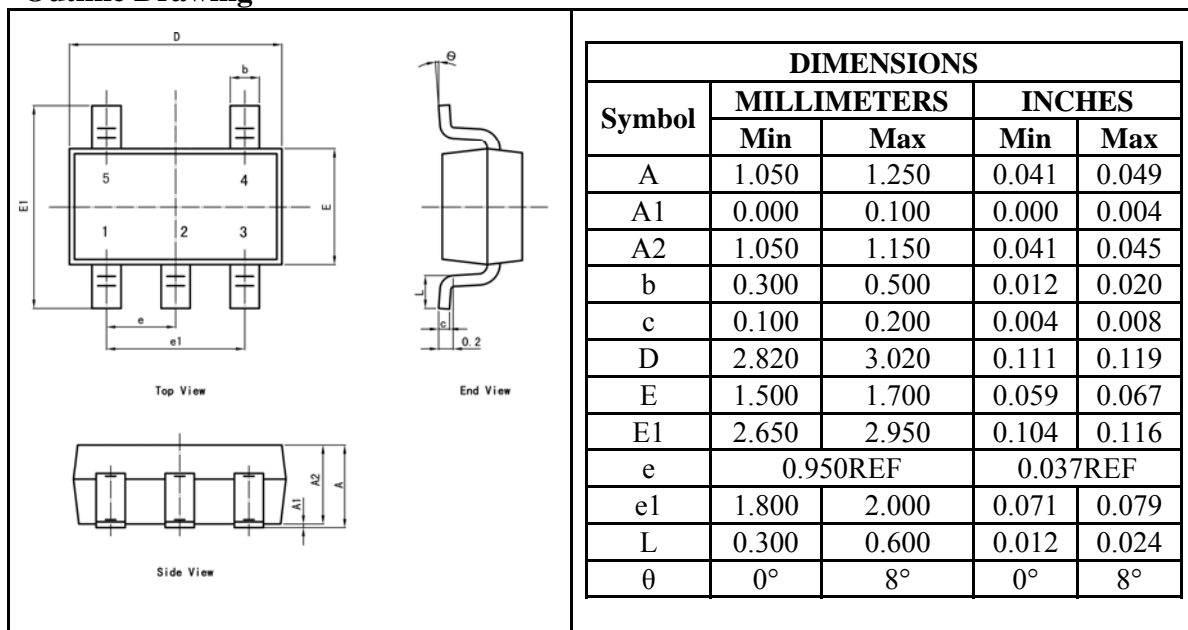
**Figure 3**

Additionally, some ceramic capacitors have a piezoelectric response. A piezoelectric device generates voltage across its terminals due to mechanical stress, similar to the way a piezoelectric accelerometer or microphone works. For a ceramic capacitor the stress can be induced by vibrations in the system or thermal transients. The resulting voltages produced can cause appreciable amounts of noise, especially when a ceramic capacitor is used for noise bypassing.

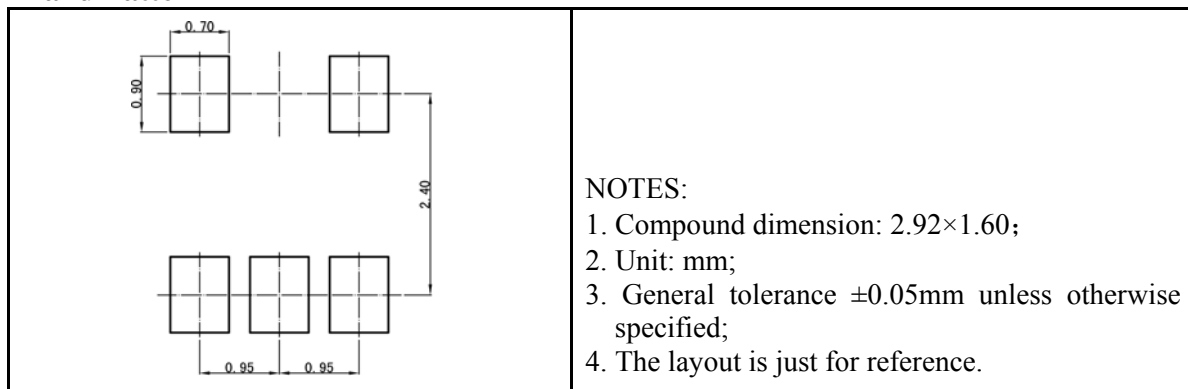
## Package Information

### UM175XX: SOT23-5

#### Outline Drawing



#### Land Pattern



#### Tape and Reel Orientation



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